

TIMETABLE FOR
M.TECH.-1ST SEMESTER REGULAR AND SUPPLEMENTARY END EXAMINATIONS, MARCH 2026
(Regular: 2025 and Supplementary: 2023 & 2024 Admitted Batches)

AR 21 Regulation - CE, EEE, ME, ECE & CSE

FN: 10.00 AM to 01.00 PM

Date	CE - TPE	EEE-PID	ME - TE	ECE - VLSI ESD	CSE - CSE
	SUBJECT & CODE				
04.03.2026 (WED)	Pavement Materials and Characterization (21TRE102)	Analysis of Power Electronics Converters (21PID101)	Advanced Fluid Mechanics (21THE001)	VLSI Technology and Design (21VLS101)	Advanced Data Structures and Algorithms (23CSE101)
06.03.2026 (FRI)	Traffic Engineering and Management (21TRE001)	Power Electronics Applications to Power Systems (21PID003)	Advanced Thermodynamics (21THE102)	Digital Design through VERILOG (21VLS002)	Advanced Operating Systems (23CSE003)
09.03.2026 (MON)	Ground Improvement Techniques (21TRE005)	Power Electronic Applications for Renewable Energy Systems (21PID004)	Finite Elements Analysis (21THE103)	DSP Processors and Architectures (21VLS005)	Data Pre-processing for Data Analytics and Machine Learning (23CSE004)
11.03.2026 (WED)	Airport Railway and Harbor Engineering (21TRE007)	Power Electronic Control of DC Drives (21PID007)	Non-Conventional Energy Sources (21THE006)	Embedded System Design (21VLS007)	Artificial Intelligence and Machine Learning (23CSE007)
13.03.2026 (FRI)	Advanced Optimization Techniques (21MEX101)				

NOTE: Student should report to the Examination Hall 30 minutes before the commencement of the Examination


ACE


Controller of Examinations